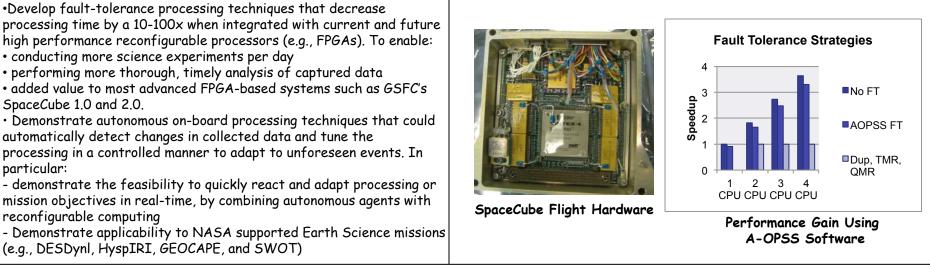


## Autonomous, On-Board Processing for Sensor Systems (A-OPSS)

PI: Matthew French, University of Southern California (USC)/ Information Sciences Institute (ISI)

## <u>Objective</u>



## Accomplishments

- Successfully developed, implemented, and tested a suite of Radiation Hardening by Software (RHBSW) techniques for embedded PowerPC cores within commercial grade FPGA devices
  - Eliminated processor "hangs"
  - Under worst-case HyspIRI orbit radiation conditions, mean time between data corruption events improved by 2.2X (to 1160 days)
  - Processor overhead to execute RHBSW techniques was <2%, yielding a 3.3x processing performance improvement over best-in-class rad-hardened processor
  - Validated the techniques via several laboratory software fault emulation campaigns and a laser injection campaign
- Demonstrated integration of processing algorithms with an on-board scheduler to perform autonomous reflectance conversion, atmospheric correction, cloud and flood classification. Resulting software performs at 150MPixel/sec and achieves data rate conversion from 804Mb/s to under 1Mb/s

Т

Co-Is/Partners: Tom Flatley, NASA GSFC

$$RL_{in} = 3$$
  $TRL_{out} = 6$ 

